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Application No.: 10/055,558

Docket No.: JCLA7148

**REMARKS****Present Status of the Application**

Upon entry of the amendments in this response, claims 1-20 are pending of which the independent claims 1, 8 and 14 have been amended without prejudice or disclaimer in order to more explicitly describe the claimed invention. It is believed that no new matter is added by a way of amendments made to the claims. For at least the foregoing reason, applicants respectfully submit that claims 1-20 patently define over prior art of record and reconsideration of this application is respectfully requested.

**Discussion for the Amended Independent Claims**

In this response, the independent claims 1, 8 and 14 have been amended to more clarify their claimed subject matters without introducing any new matter. Besides, the amendments made to the independent claims 1, 8 and 14 are supported by the specification of the present invention. In the amended independent claim 1, the amended first step of the method, "recording a plurality of write data stored inside the second-to-first posted write buffer while the bridging device executes a first-to-second bus read operation," is supported in lines 5-7 in the paragraph [0016] in the specification. Moreover, the amended second step of the method, "holding a response data issued from the second bus after executing the first-to-second bus read operation on the second bus," is supported in the line 7 in the paragraph [0018] and lines 3-6 the paragraph [0023] in the specification. Also, the amended third step of the method, "transmitting the response data to the first bus as soon as the write data are completely transmitted to the first

## BEST AVAILABLE COPY

Application No.: 10/055,558

Docket No.: JCLA7148

bus," is supported in the last sentence in the paragraph [0020]. Likewise, the preceding descriptions can be applied to the amended independent claims 8 and 14.

### Discussion for objection to the title of the present invention

*1. There is a typographical error, "bridging," in the title of the present invention.*

In response thereto, applicants appreciated the examiner's pointing out the preceding error occurred in the title of the present invention. Accordingly, the title of the present invention was corrected to be "DATA TRANSMISSION SEQUENCING METHOD ASSOCIATED WITH BRIDGING DEVICE AND APPLICATION SYSTEM."

### Discussion for objection to claims under 35 U.S.C.103 (a)

*5. Claim 1 is rejected under 35 U.S.C. 103(a) as being anticipated over Applicant's Admitted Prior Art (AAPA) and U.S. 6,081,851 (Futrel et al. hereinafter referred to Futrel). Further, AAPA teaches the method comprises recording a plurality of write data stored inside the write buffer (Fig.2, SP\_w[1:0]) when a first-to-second read operation (Fig.2, PS\_r\_a) occurs, and transmitting the response data to the first bus after transmitting the write data to the first bus (Fig.2, PS\_r\_d). More, Futrel teaches the way to hold/transmit the data from one bus to another (col. 1, line 60-col. 2, line 10)*

**BEST AVAILABLE COPY**

Application No.: 10/055,558

Docket No.: JCLA7148

In response thereto, applicants respectfully traverse the preceding objections based on the following arguments and thus withdrawal of objections to the claim 1 is respectfully requested.

To establish a prima facie case of obviousness, the cited references (i.e. AAPA and Futral) should teach every limitation disclosed in the independent claims 1, 8 and 14. First of all, in our opinion, the examiner mistook a first-to-second read operation to be denoted by "PS\_r\_a", which should be referred as a first-to-second data read request, as disclosed in the lines 5-6 in the paragraph [0004] in the specification. Instead, the first-to-second read operation should be denoted by PS\_r. Therefore, AAPA does fail to disclose the first step of the method as claimed in the amended claim1. Also, AAPA (particularly, shown in Fig.2) fails to teach, suggest or disclose the third step of the method, "transmitting the response data to the first bus as soon as the write data are completely transmitted to the first bus," as claimed in the amended claim 1. Instead, AAPA teaches the timing of transmitting the response data (PR\_r\_d) to the first bus, is delayed by a session after finishing transmitting the write data to the first bus.

Furthermore, Futral only discloses a conventional memory-accessing technology, but does not disclose "when to obtain the data," "where the data come from," and "what attribute (or called "usage") of the data" at all. Accordingly, Futral fails to teach, suggest or disclose the amended second step of the method, "holding a response data issued from the second bus after executing the first-to-second bus read operation on the second bus," as claimed in the amended claim 1. Therefore, according to the preceding discussions, even if the AAPA and Futral could be incorporated, this incorporation still fails to teach, suggest or disclose all limitations of the amended independent claim 1; that is, this incorporation can not render the amended independent

**BEST AVAILABLE COPY**

Application No.: 10/055,558

Docket No.: JCLA7148

claim 1 "obvious." In other words, the amended independent claim 1 is patentable over AAPA, and further in view of Futral under 35 U.S.C.103 (a).

Regarding dependent claims 2-7, no matter whether they are conventional, they should be patentable as a matter of law for the reason that they contain all limitations of their patentable base independent claim 1.

*5. Claim 8 is rejected under 35 U.S.C. 103(a) as being anticipated over Applicant's Admitted Prior Art (AAPA) and U.S. 6,081,851 (Futral et al. hereinafter referred to Futral).*

In response thereto, applicants respectfully traverse the preceding objections based on the following arguments and thus withdrawal of objections to the claim 8 is respectfully requested.

To establish a prima facie case of obviousness, the cited references (i.e. AAPA and Futral) should teach every limitation disclosed in the amended independent claim 8. As discussed above, the incorporation of AAPA and Futral fail to teach, suggest or disclose the second step and the third step of the method as claimed in the amended independent claim 1. Accordingly, this incorporation fail to teach, suggest or disclose "a hold buffer for holding a response data issued from the second bus after executing a read operation on the second bus, and transmitting the response data to the first bus as soon as the write data, which are issued before the read operation, are completely transmitted to the first bus," as claimed in the amended independent claim 8. That is, this incorporation can not render the amended independent claim 8 "obvious." In other words, the amended independent claim 8 is patentable over AAPA, and further in view of Futral under 35 U.S.C.103 (a).

## BEST AVAILABLE COPY

Application No.: 10/055,558

Docket No.: JCLA7148

Regarding dependent claims 9-13, no matter whether they are conventional, they should be patentable as a matter of law for the reason that they contain all limitations of their patentable base independent claim 8.

*18. Claim 14 is rejected under 35 U.S.C. 103(a) as being anticipated over Applicant's Admitted Prior Art (AAPA) and U.S. 6,081,851 (Futral et al. hereinafter referred to Futral).*

In response thereto, applicants respectfully traverse the preceding objections based on the following arguments and thus withdrawal of objections to the claim 14 is respectfully requested.

To establish a prima facie case of obviousness, the cited references (i.e. AAPA and Futral) should teach every limitation disclosed in the amended independent claim 14. As discussed above, the incorporation of AAPA and Futral fail to teach, suggest or disclose the third step of the method as claimed in the amended independent claim 1. Accordingly, this incorporation fail to teach, suggest or disclose "the bridging device transmits the response data to the first bus as soon as a plurality of write data, which are issued before the read operation, are completely transmitted to the first bus" as claimed in the amended independent claim 14. That is, this incorporation can not render the amended independent claim 14 "obvious." In other words, the amended independent claim 14 is patentable over AAPA, and further in view of Futral under 35 U.S.C.103 (a).

Regarding dependent claims 15-20, no matter whether they are conventional, they should be patentable as a matter of law for the reason that they contain all limitations of their patentable base independent claim 14.

**BEST AVAILABLE COPY**

Application No.: 10/055,558

Docket No.: JCLA7148

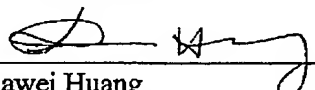
**CONCLUSION**

For at least the foregoing reasons, it is believed that all the pending claims 1-20 of the present application patentably define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date: 8/3/2005

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Respectfully submitted,  
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